



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

## \*BIBDATASHEET\*

CONFIRMATION NO. 6382

Bib Data Sheet

SERIAL NUMBER 09/987,001	FILING DATE 11/13/2001  RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 60188-402
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Yoshinori Odake, Hirakata-shi, JAPAN;

Fumihiko Noro, Kyotanabe-shi, JAPAN;

Takahiko Hashidzume, Takasuki-shi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

NONE

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-032573 02/08/2001

JAPAN 2001-110982 04/10/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/16/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

## ADDRESS

20277  
 MCDERMOTT WILL & EMERY LLP  
 600 13TH STREET, N.W.  
 WASHINGTON, DC  
 20005-3096

## TITLE

Method of fabricating nonvolatile semiconductor memory device

FILING FEE  RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
-----------------------------------	---	--